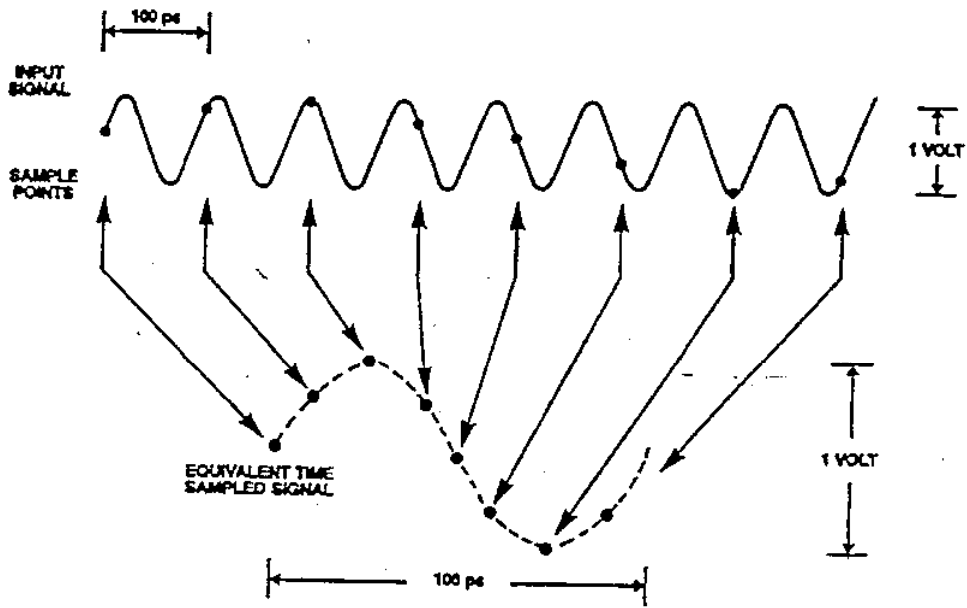
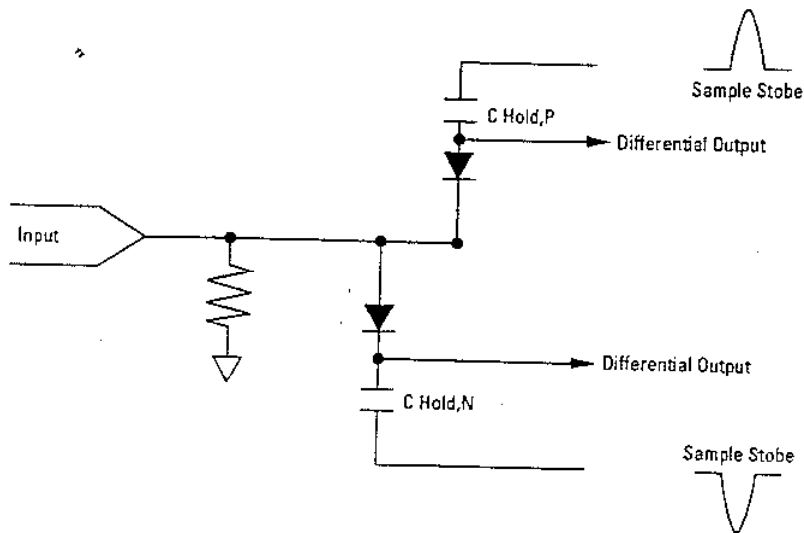
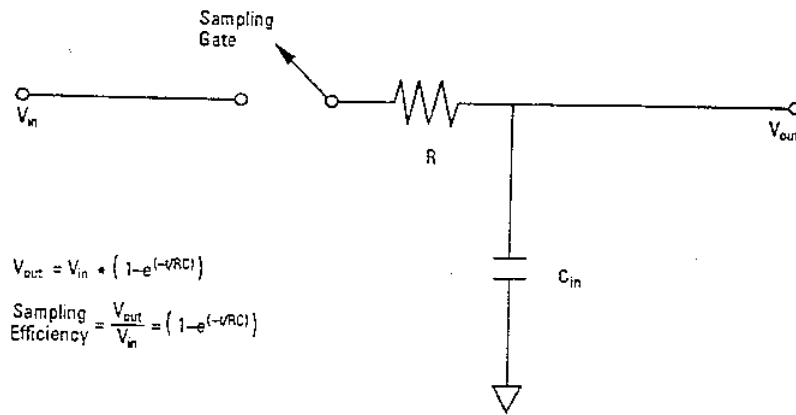


# Sampling Oscilloscope



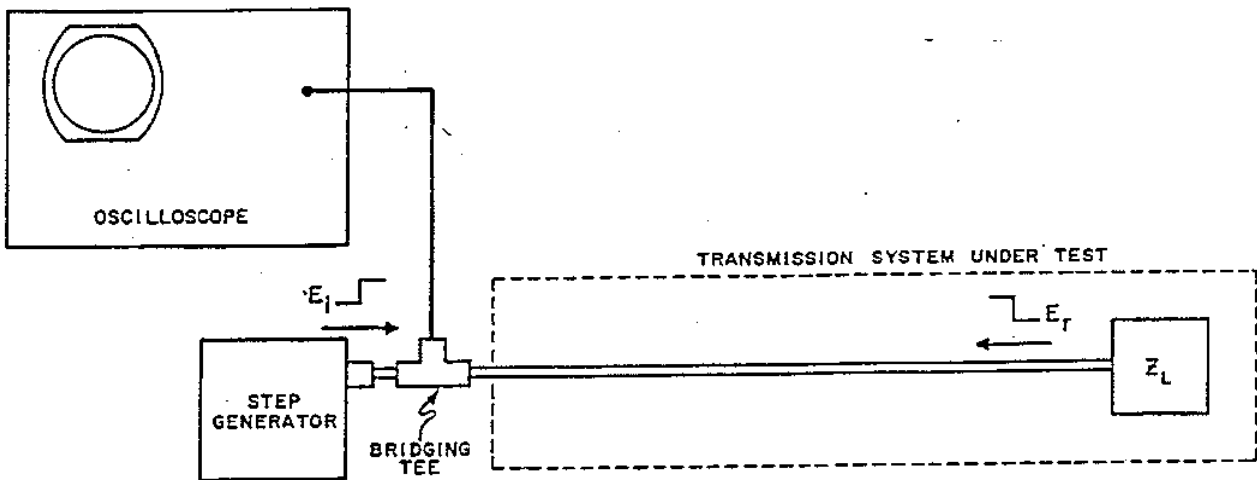
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# Sampling Scope Front End

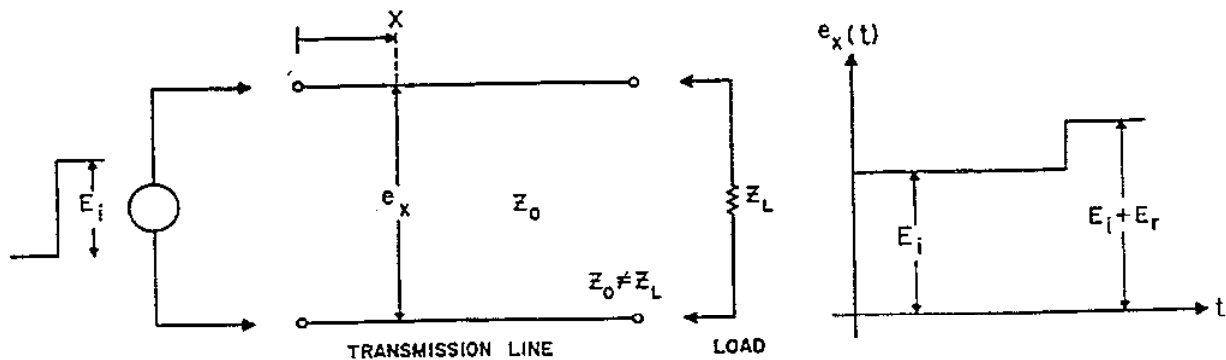


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# Time Domain Reflectometer (TDR)



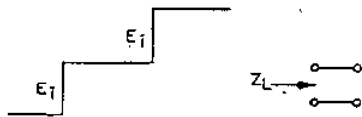
Once the incident and reflected voltages are measured on the oscilloscope, the reflection coefficient and impedance of the mismatch may be calculated.



$$\rho = E_r/E_i = (Z_L - Z_0)/(Z_L + Z_0)$$

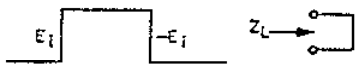
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# TDR Displays for Resistive Loads



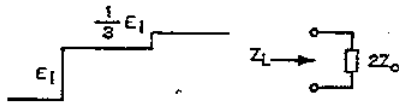
(A) OPEN CIRCUIT TERMINATION ( $Z_L = \infty$ )

(A)  $E_r = E_i$       THEREFORE  $\frac{Z_L - Z_0}{Z_L + Z_0} = +1$   
 WHICH IS TRUE AS  $Z_L \rightarrow \infty$   
 $\therefore Z = \text{OPEN CIRCUIT}$



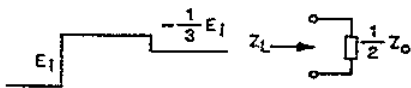
(B) SHORT CIRCUIT TERMINATION ( $Z_L = 0$ )

(B)  $E_r = -E_i$       THEREFORE  $\frac{Z_L - Z_0}{Z_L + Z_0} = -1$   
 WHICH IS ONLY TRUE (FOR FINITE  $Z_0$ )  
 WHEN  $Z_L = 0$   
 $\therefore Z = \text{SHORT CIRCUIT}$



(C) LINE TERMINATED IN  $Z_L = 2Z_0$

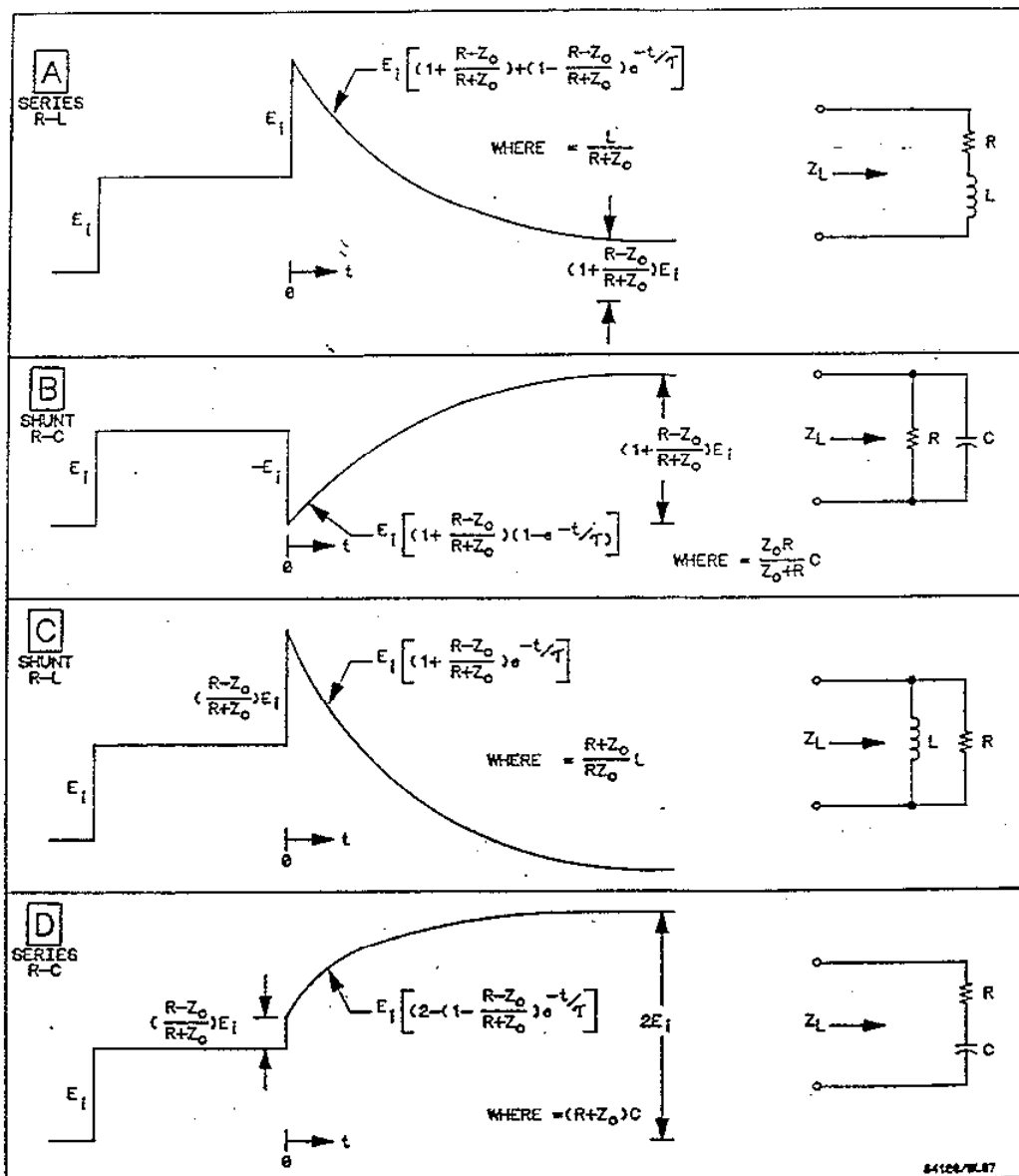
(C)  $E_r = +\frac{1}{3} E_i$       THEREFORE  $\frac{Z_L - Z_0}{Z_L + Z_0} = +\frac{1}{3}$   
 AND  $Z_L = 2Z_0$



(D) LINE TERMINATED IN  $Z_L = \frac{1}{2} Z_0$

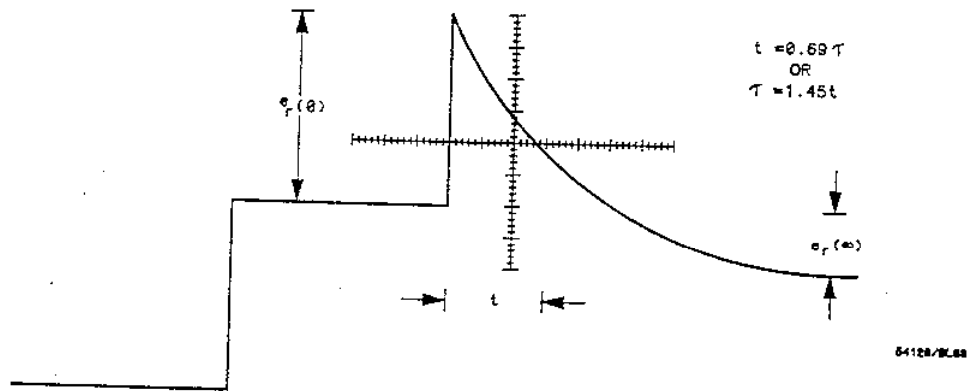
(D)  $E_r = -\frac{1}{3} E_i$       THEREFORE  $\frac{Z_L - Z_0}{Z_L + Z_0} = -\frac{1}{3}$   
 AND  $Z_L = \frac{1}{2} Z_0$

# TDR Displays for Complex Loads

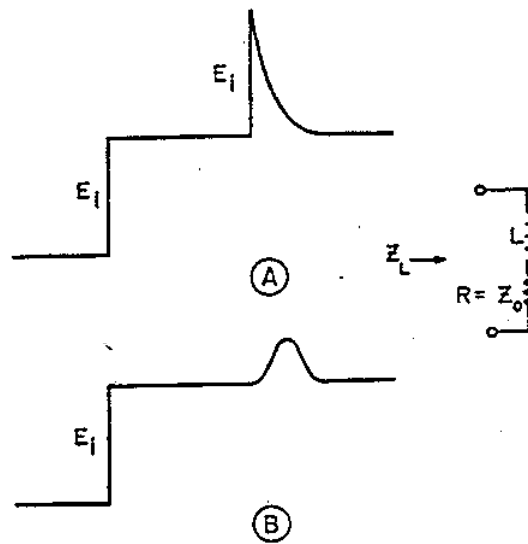


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# TDR Measurement of Time Constants



# TDR Displays with Limited Bandwidth



## References

TDR Fundamentals, Hewlett Packard App. Note 62

HP teaching tools WWW page, <http://www.tmo.hp.com/tmo/iaa/edcorner>

High Bandwidth Oscilloscope Sampling Architectures, HP product note 54120-3, August 1989